



Thin Film Circuit

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◆Product Features

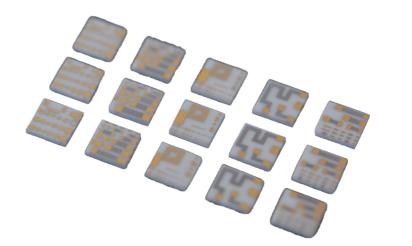
- 1. Sputtering technology, high reliability and ultra-stable performance, good consistency.
- 2.Designed and processed with 99.6% pure Al2O3 substrate, which has excellent insulation performance and low loss at high frequency.
- 3. Designed and processed with high-purity AIN substrate, which has excellent thermal conductivity.

♦Product Applications

Substrates for microwave/millimeter wave application, microwave/millimeter wave device, and high-speed optical communication device.

♦ Process Introduction

On the ceramic substrate, through magnetron sputtering, photoetching, dry wet etching, electroplating gold and other processes, the thin film components and metal lines are integrated to form high-precision circuit patterns with specific functions.



◆ Material Properties

Material	Chemical Composition	Purity	Color	Nominal Density (g/cm3)	Loss (1 MHz)	Dielectric Constant (1 MHz)	Thermal Conductivity (W/m°K)	CTE (10 ⁻⁶ mm/° C)
Aluminum Oxide	Al ₂ O ₃	96%	White	3.7	0.0003	9.5±0.2	24.7	6.5~8.0 (25°C~800°C)
Aluminum Oxide (Polished)	Al ₂ O ₃	99.6%	White	3.87	0.0001	9.9±0.1	26.9	7.0~8.3 (25°C~1000°C)
Aluminum Oxide (As-fired)	Al ₂ O ₃	99.6%	White	3.87	0.0001	9.9±0.1	26.9	7.0~8.3 (25°C~1000°C)
Aluminum Nitride (Polished)	AlN	98%	Gray	3.28	0.001	8.8±0.2	170	4.6 (25℃~300℃)
Aluminum Nitride (As-fired)	AIN	98%	Gray	3.28	0.001	8.8±0.2	170	4.6 (25℃~300℃)



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♦ Design Guidelines

Substrate Materials

- 1. Material: alumina oxide, aluminum nitride, silicon, glass, etc.
- 2. Layout: $2 \sim 6$ inches square or round (Typical: 2 inches square)
- 3. Thickness: $0.101 \sim 1.524 \text{ mm}$ (Typical: 0.254, 0.381)
- 4.Roughness: polished(<0.08µm), as-fired(<0.2µm), lapped (customer specified)

Metal

- 1. Sputtering: Ti、TiW、TaN、Cu、Ni、Pt、Au
- 2. Electroplating: Au
- 3. Au thickness: $0.5 \sim 5 \mu m$

TaN Sheet Resistance

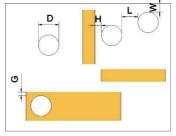
- 1. Sheet resistance: $25 \sim 200\Omega/\Box$ (Typical: $50\Omega/\Box$)
- 2. Resistance tolerance: $\pm 10\%$ (Typical: $\pm 20\%$)
- 3. Minimum resistor size: 50µm*50µm
- 4. Resistance TCR: -100 ± 50 ppm/°C @ -55°C $\sim +125$ °C
- 5. Maximum service temperature: 350°C (<0.5 hours)

Graphic

- 1. Minimum line width: 10µm
- 2. Minimum line gap: 20µm
- 3. Line tolerance: $\pm 3\mu m$ (for non-critical areas $\pm 5\mu m$)

Metallized holes/slots

- 1. Hole diameter D: 0.5*T minimum
- 2. Spacing between via holes L: 1*T minimum
- 3. Hole to edge W: 1*T minimum
- 4. Hole to metal line H: 38.1µm minimum
- 5. Via hole to conductor edge G: 50.8µ minimum





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Dimensions

- 1. Minimum size: 0.3mm*0.3mm
- 2. Tolerance: ± 0.05 mm

Drawing

- 1. Format: DXF、DWG
- 2. Length unit: mm

Detailed Design Guidelines

