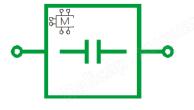
CAP-DLP-0805-001

##Modelithics®

surface mount chip capacitor model

Model Features*

- Broadband validation: DC 40 GHz
- Equivalent circuit based
- Substrate scalable: $(0.9 \le H/Er \le 17 \text{ mil})$
- Part value scalable: (0.2 to 200 pF)
- Land Pattern (Pad) scalable
- Orientation Selectable (H/V)
- Validation: Equivalent series resistance
- Developed for microstrip interconnects
 - * See Technical Notes for more details



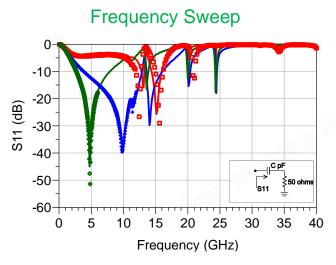
CAP-DLP-0805-001 (0.2 to 200 pF) 0805 Body Style

Model Description

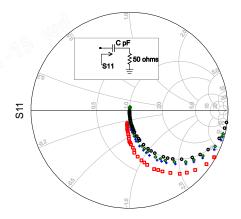
The CAP-DLP-0805-001 is a substrate scalable Microwave Global Model[™] for the Dalicap P/N DLP75D surface mount chip capacitor family (additional information is available at <u>www.dalicap.com</u>). The models are for use with microstrip applications and account for substrate (or printed circuit board) related parasitic effects. Substrate height, dielectric constant, loss tangent, interconnect metal thickness, component tolerance, component value, pad width, pad length, and pad gap, and orientation are model input parameters. Models account for up to two higher-order resonant frequency pairs beyond the fundamental series resonant frequency. The model is validated with measured equivalent series resistance (ESR). A single, substrate scalable, pad scalable, and orientation selectable Microwave Global Model[™] is available that accurately emulates all capacitor values within the valid capacitance range. A Sim_mode switch allows pad stack effects to be disabled.

Model simulation may vary slightly based on simulator used.

The pad dimensions used to develop datasheet plots for the model are: length = 31.5 (0.800), width = 51.2 (1.300), gap = 39.4 (1.000). Units in mil (mm).



Part Value Sweep



Legend: \Box 4 mil Rogers 4350B, + 20 mil Rogers 4350B, \diamond 60 mil Rogers 4003C.Lines - Model, Symbols - Measured data. Measured data stops at highest valid frequency for each substrate. S11 for a 1.2 pF capacitor mounted on various substrates from 0.045 to 40.0 GHz.

Legend: \Box 4 mil Rogers 4350B, + 20 mil Rogers 4350B, \Diamond 60 mil Rogers 4003C O Ideal

Model S11 at 1.0 GHz for capacitor values from 0.2 to 200 pF on various substrates compared to an ideal capacitor response.

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-0

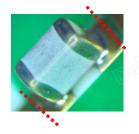
Technical Notes

- Two-port S-parameters were measured using a vector network analyzer and onboard probing with calibration referenced to the outside edges of the component pad stack.
- Capacitors were measured in a 2-port series configuration using a 50-ohm microstrip test fixture. Models for alternative interconnect configurations (e.g. coplanar waveguide) are available upon request.
- Nominal part value range (0.2 to 200 pF)
 - Tolerance low value: ±0.1pF
 - Tolerance on high value: 5%
- Pad scalable models are validated with S-parameter measurements within the recommended pad range.
- Substrates used to extract the models: 4 mil Rogers 4350B, 20 mil Rogers 4350B, and 60 mil Rogers 4003C.
- Measurement validated substrate range of substrate height and dielectric constant ratios based on substrates used to develop model:

1 ≤ H/Er ≤ 16.4 (mil) 0.02 ≤ H/Er ≤ 0.42 (mm)

- Equivalent series resistance (ESR) was measured using a Boonton 34A resonant line.
- Highest frequency for measurement validation: 40 GHz (4 mil Rogers 4350B), 12 GHz (20 mil Rogers 4350B), and 6 GHz (60 mil Rogers 4003C)
- Multiple simulation modes (Sim_mode) are available full mode, ideal mode and no pad stack.

Device Image

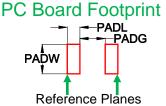


Reference Planes at pad edge

Capacitor Values (pF)

-											
0.1	1	0.2	0.3	0.4	0.5	0.6	0.7	0.8	0.9	1	1.2
1.5	5	1.6	1.8	2	2.2	2.4	2.7	3	3.3	3.9	4.7
5		5.6	6.8	7.5	8.2	9.1	10	12	15	18	20
22	2	24	27	30	33	39	47	51	56	62	68
82	2	91	100	120	150	180	200	220	2		

Highlighted capacitor values are measurement-based models. Other models found via interpolation. Table shows 52-part values in the model range based on manufacturer's datasheet.



31.5 (0.800) <= PADL <=53.1 (1.350) 51.2 (1.300) <= PADW <=61.0 (1.550) 25.6 (0.650) <= PADG <= 39.4 (1.000) Units in mil (mm)

Model Input Parameters

- C Nominal component value in pF. The full parasitic model is invoked if the part value is within the valid limits of the model, otherwise an ideal element model is used.
- Subst Microstrip substrate instance name. The model will reference the named substrate instance to obtain values for H, Er, T and TanD.
- Sim_mode 0 for full parasitic model, 1 for ideal element, 2 for removing pad effects, 3 for simplified parasitic model.
- Pad_mode 0 for default to Sim_mode, 1 for pads always in layout, 2 for pads never in layout.
- Tolerance Tolerance of the part value. The nominal value for this parameter should be set to 1. Use for statistical distribution.
- Pad_Width Width of land pattern footprint
- Pad_Length Length of land pattern footprint
- Pad_Gap Pad to pad spacing (inside pad edge - to - inside pad edge)
- Orient 0 for Horizontal, 1 for Vertical
- C_Discrete Discrete input parameter based on manufacturer available part values can be used for tuning and optimization. Overrides C input parameter.

CAP-DLP-0805-001

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S11 (dB)

S11 (dB)

0

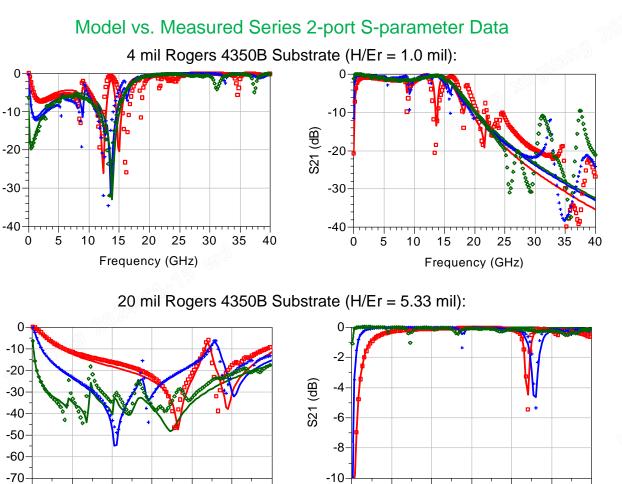
2

4

6

Frequency (GHz)





2

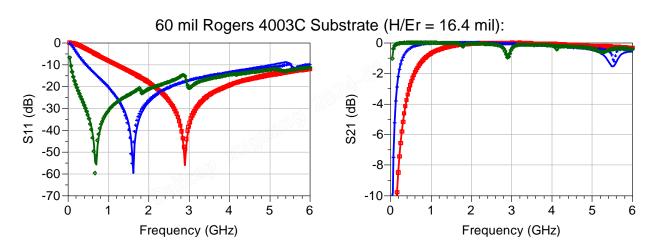
4

6

Frequency (GHz)

8

0



10

12

8

Legend:
3.3 pF, + 10 pF,
68 pF, Solid lines - Model data, Symbols - Measured data

CAP-DLP-0805-001

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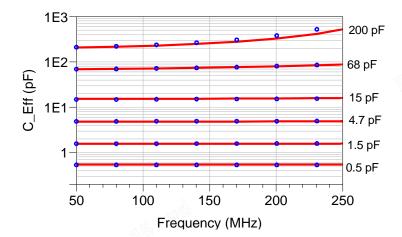
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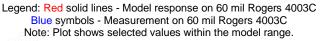
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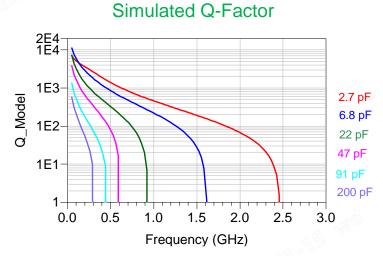
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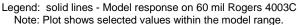


Effective Capacitance









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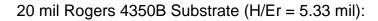
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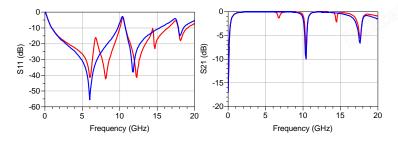
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##Modelithics®

Horizontal vs. Vertical Model Comparison





Legend: 5 pF Capacitor. Red Line - Horizontal Model performance and Blue line - Vertical Model performance

Model and Datasheet Revision Notes

12/13/2023

Original model and datasheet development

